



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of: )  
LENOBLE )  
Serial No. 10/714,440 )  
Confirmation No. 9369 )  
Filing Date: NOVEMBER 14, 2003 )  
For: PROCESS FOR FABRICATING A )  
SHORT-GATE LENGTH MOS )  
TRANSISTOR AND INTEGRATED )  
CIRCUIT COMPRISING SUCH A )  
TRANSISTOR )

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop: PGPUB-Drawings  
Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

Enclosed are five (5) sheets of formal drawings to be  
filed in the above-identified patent application.

Respectfully submitted,

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